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JUN 05 2006

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*Ramyar M. Farid*  
6/5/06Signature Date **FACSIMILE****Date:** June 5, 2006**Time Sent:**

<b>To:</b>	<b>Company:</b>	<b>Facsimile No:</b>	<b>Telephone No:</b>
Examiner Michael Rutland Wallis	U.S. Patent and Trademark Office	571-273-8300	
<b>From:</b>	Ramyar M. Farid	<b>Direct Phone:</b>	202.756.8146
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<b>Sent By:</b>	Matilda Mason	<b>Direct Phone:</b>	202-756-8661
<b>Client/Matter/Tkpr:</b>	60188-662/5328	<b>Original to Follow by Mail:</b>	No
		<b>Number of Pages, Including Cover:</b>	15
<b>Re:</b>	U.S. Patent Application No.: 10/679,365 Attorney Docket No.: 60188-662		

**Message:**

This Amendment is in response to Office Action dated March 3, 2006

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Main Facsimile: 202.756.8087 Facsimile Operator: 202.756.8090

U.S. practice conducted through McDermott Will & Emery LLP.  
600 Thirteenth Street, N.W. Washington, D.C. 20005-3096

Telephone: 202.756.8000

Docket No.: 060188-0662

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**RECEIVED**  
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**JUN 05 2006**

In re Application of

Customer Number: 20277

Ryota NISHIKAWA, et al.

Confirmation Number: 3864

Application No.: 10/679,365

Group Art Unit: 2835

Filed: October 07, 2003

Examiner: RUTLAND WALLIS, MICHAEL

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD FOR DESIGNING THE SAME

Mail Stop Amendment  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

☒  
☐  
☐

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	35	35	0	\$50.00 =	\$0.00
Independent Claims	2	3	0	\$200.00 =	\$0.00
Multiple dependent claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00.

☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL &amp; EMERY LLP

*[Signature]*  
 Ramyar M. Farid  
 Registration No. 46,692

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 Washington, DC 20005-3096  
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 Date: June 5, 2006

Please recognize our Customer No. 20277 as our  
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**RECEIVED  
CENTRAL FAX CENTER****JUN 05 2006**

Docket No.: 060188-0662

**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
	:	
Ryota NISHIKAWA, et al.	:	Confirmation Number: 3864
	:	
Application No.: 10/679,365	:	Group Art Unit: 2835
	:	
Filed: October 07, 2003	:	Examiner: RUTLAND WALLIS, MICHAEL
	:	
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD FOR DESIGNING THE SAME		

**AMENDMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 3, 2006, having a shortened statutory period for response set to expire June 3, 2006 (Saturday), please amend the above-identified application as follows: